



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

ITOH et al.

Art Unit: 2819

Application No.: 09/735,555 ✓

Examiner: D. Chang

Filed: December 14, 2000

Attorney Dkt. No.: 108075-00022

For: OUTPUT BUFFER CIRCUIT AND CONTROL METHOD THEREFOR

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Date: August 29, 2002

Sir:

This paper responds to the Office Action dated April 20, 2002, the time period for response having been extended from July 30, 2002 until August 30, 2002 by the enclosed Petition for Extension of Time. In the prosecution of the application identified above, please consider the following amendments:

IN THE CLAIMS:

Please cancel claims 12-18.

Please amend claims 1-7 and 11 as follows:

1 (Once Amended) A method of controlling an output buffer circuit for generating an output signal and outputting the output signal from an output terminal, wherein the output buffer circuit includes a first drive circuit for receiving an input signal,

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